

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Rapport, et al.	Art Unit :	2841
Serial No. :	10/804,452	Examiner :	Andargie M Aychillhum
Filed :	March 19, 2004	Conf. No. :	7279
Title :	MEMORY EXPANSION AND INTEGRATED CIRCUIT STACKING SYSTEM AND METHOD		

**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF AUGUST 6, 2008

Please amend the above-identified application as follows:

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.

**CERTIFICATE OF MAILING BY EFS-WEB FILING**

I hereby certify that this paper was filed with the Patent and Trademark Office using the EFS-WEB system on this date: December 08, 2008